

FORM PTO-1449 INFORMATION DISCLOSURE CITATION IN AN APPLICATION <i>(Use several sheets if necessary)</i>	Docket Number (Optional) 81790.0308	Application Number 0/762817 Not assigned
	Applicant SHIBATA et al.	
	Filing Date January 21, 2004	Group Art Unit 2824 Not assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
gu	6,122,193	9/2000	SHIBATA et al.	—	—	
	6,178,115	1/2001	SHIBATA et al.	—	—	
	5,892,710	4/1999	FAZIO et al.	—	—	
	6,055,188	4/2000	TAKEUCHI et al.	—	—	
	6,058,042	5/2000	NOBUKATA	—	—	
	5,892,710	4/1999	FAZIO et al.	—	—	
	5,949,708	9/1999	CHEVALLIER	—	—	
	6,034,892	3/2000	CHOI	—	—	
	6,040,992	3/2000	CHEVALLIER	—	—	
gu	6,141,243	10/2000	ASLAM et al.	—	—	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
gu	0-797212	9/24/97	EP	—	—		
gu	97-48098	12/18/97	WO	—	—		
gu	0-908894	4/14/99	EP	—	—		

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

gu	Ken Takeuchi et al. "A multi-page cell architecture for high-speed programming multi-level NAND flash memories", No. XP002132533; 1997 Symposium on VLSI Circuit Digest of Technical Papers; pg. 67-68

EXAMINER <i>Vu Le</i>	DATE CONSIDERED <i>04/03/05</i>
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.	